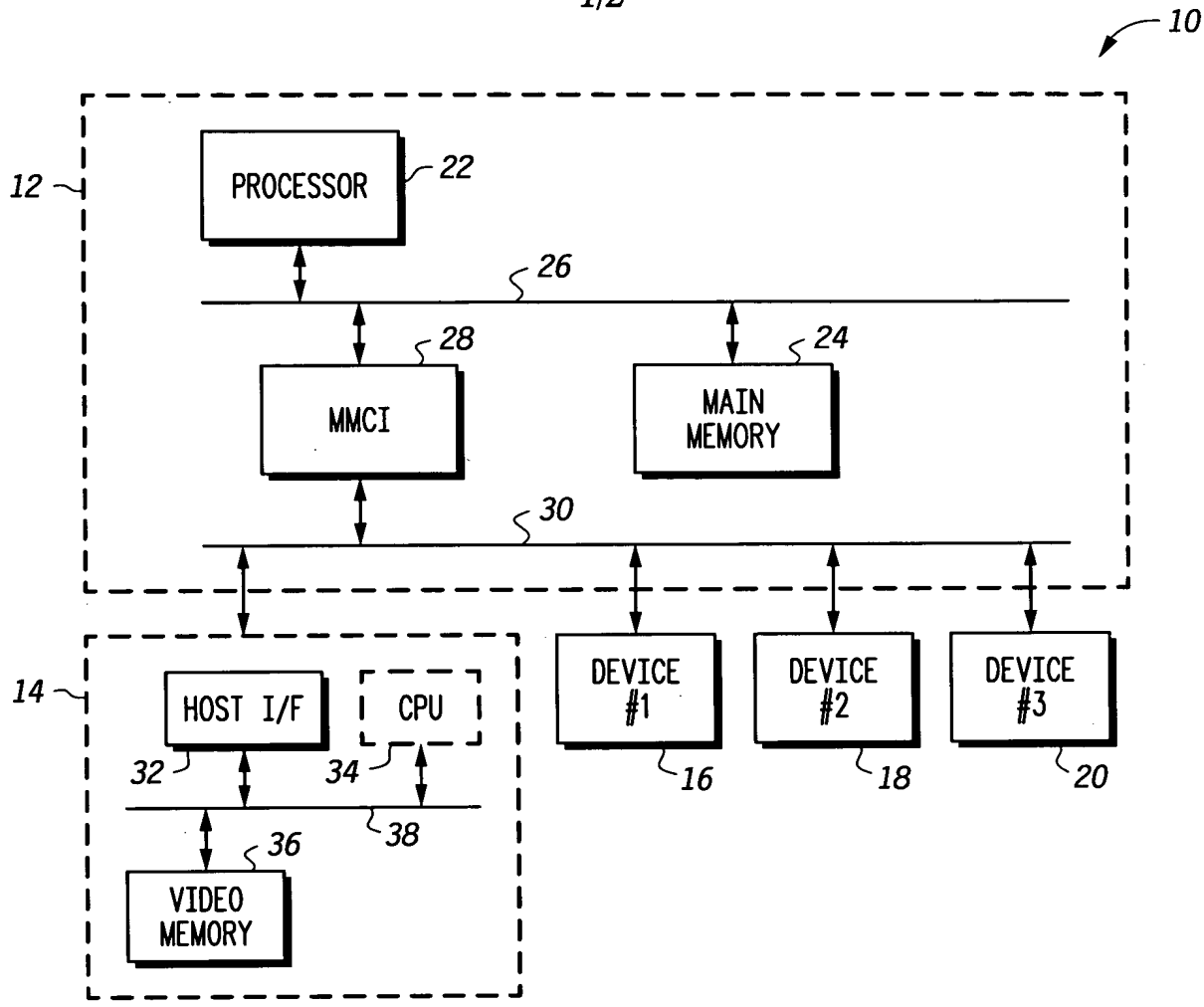
10
12
PROCESSOR
22
26
28
MMCI
24
MAIN MEMORY
30
14
HOST I/F
CPU
32
34
38
36
VIDEO MEMORY
DEVICE #1
DEVICE #2
DEVICE #3
16
18
20

FIG. 1

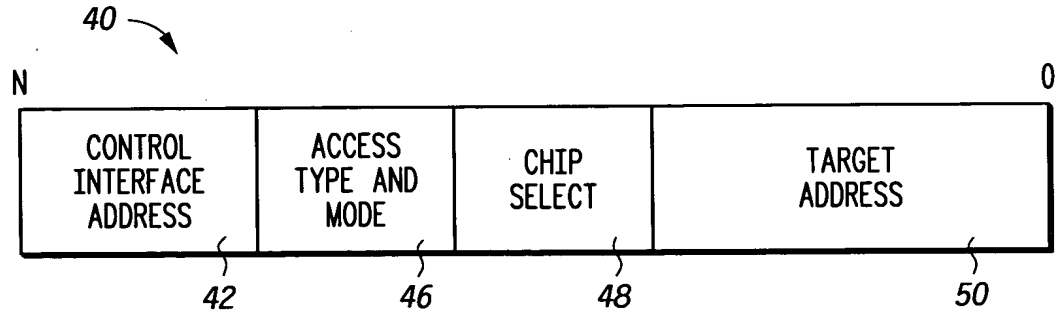
40
N
0
CONTROL INTERFACE ADDRESS
ACCESS TYPE AND MODE
CHIP SELECT
TARGET ADDRESS
42
46
48
50

FIG. 2

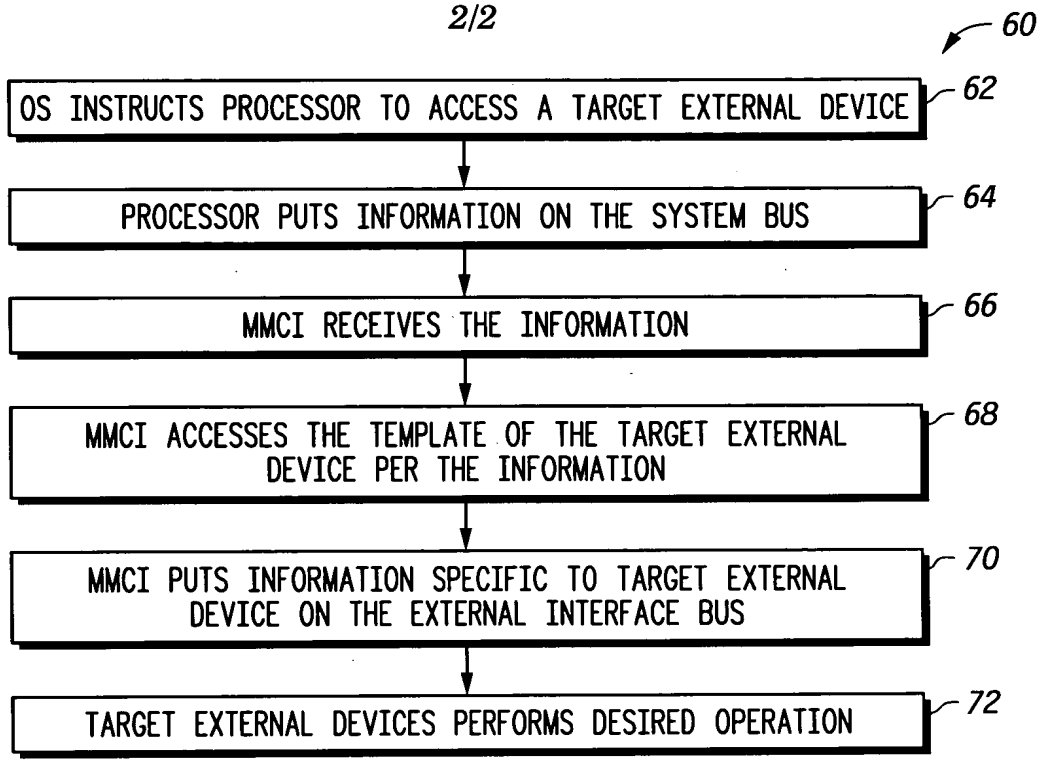
60
OS INSTRUCTS PROCESSOR TO ACCESS A TARGET EXTERNAL DEVICE
62
PROCESSOR PUTS INFORMATION ON THE SYSTEM BUS
64
MMCI RECEIVES THE INFORMATION
66
MMCI ACCESSES THE TEMPLATE OF THE TARGET EXTERNAL DEVICE PER THE INFORMATION
68
MMCI PUTS INFORMATION SPECIFIC TO TARGET EXTERNAL DEVICE ON THE EXTERNAL INTERFACE BUS
70
TARGET EXTERNAL DEVICES PERFORMS DESIRED OPERATION
72

FIG. 3

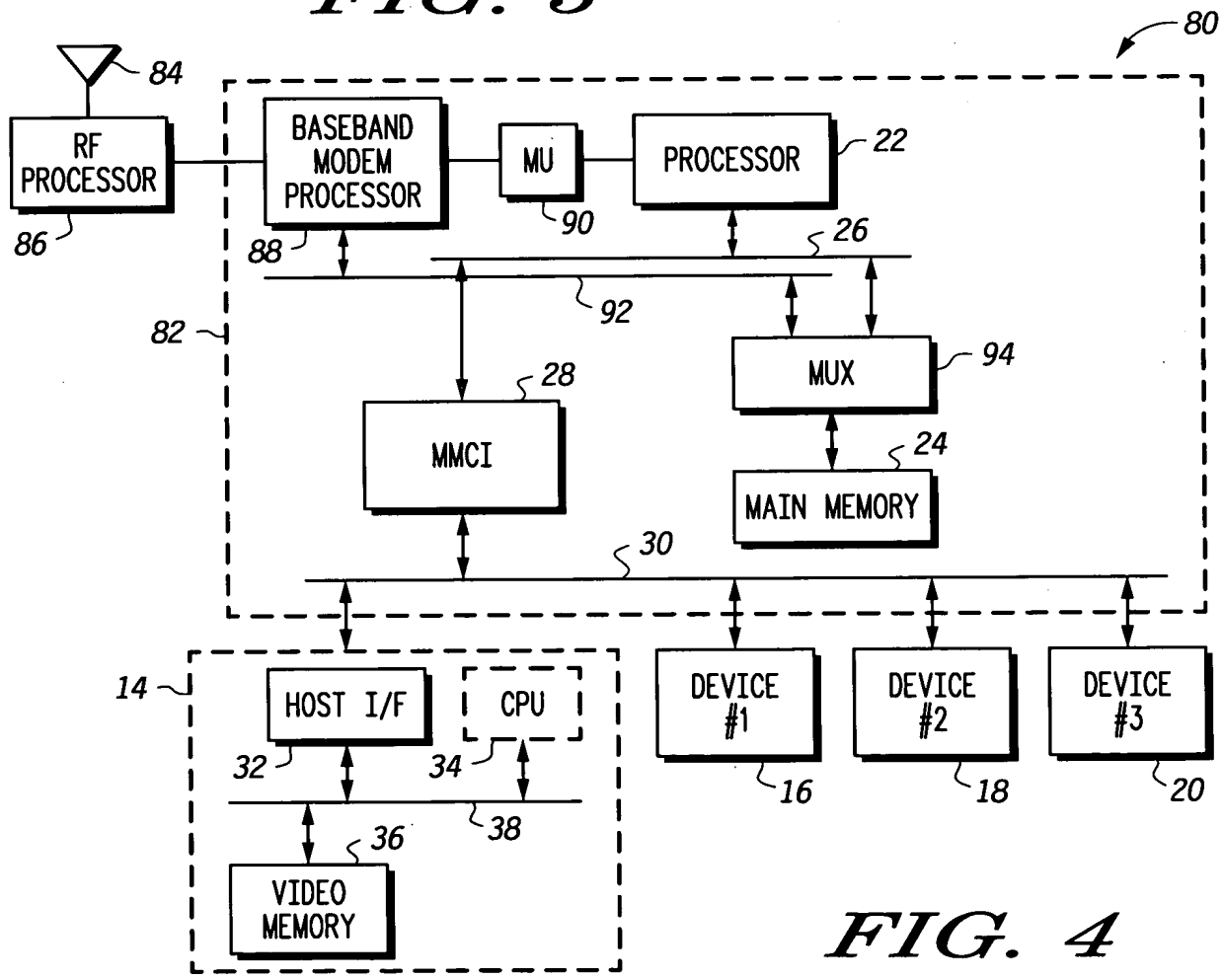
80
84
RF PROCESSOR
86
BASEBAND MODEM PROCESSOR
88
MU
90
PROCESSOR
22
26
92
82
MUX
94
28
MMCI
24
MAIN MEMORY
30
14
HOST I/F
32
CPU
34
38
36
VIDEO MEMORY
DEVICE #1
16
DEVICE #2
18
DEVICE #3
20

FIG. 4